

AQ-110P SS:2a

ON SW2 OFF	ON SW1 OFF
T1/T2 latch: On / Off	S1: L> / L> + I>
Pulse blocking: On / Off	S2: L> / L> + I>
(reserved)	L> / L> + I>
S5: Fiber loop / AQD	a / b
FAST / CBFP	
100 ms / 150 ms	
I>: 1 A / 5 A	
IO>: 1 A / 5 A	
	Scheme selection

SS:2a	T1	T2	T3	BO1	HSO1	HSO2	AQD
S1	x ¹	x ¹	CBFP ¹			CBFP ¹	
S2	x ¹	x ¹	x ¹			x ¹	x ²
S3,S4,S5	x ¹	CBFP ¹	x ¹			x ¹	x ²
Lex> (BI2)	x ¹	CBFP ¹	x ¹			x ¹	x ²
lex> (BI1)					x		
I> (phase currents)				x	x		
Io> (residual current)				x	x		

- 1) Activates only if channel has been set to light only mode or if any overcurrent signal (I>, Io> or BI1) is ON.
- 2) Activates only if phase overcurrent signal (I>) is ON.

AQ-101 SS:1

ON SW1 OFF
L> / L> + I>
S1: L> / L> + I>
Latch: On / Off
100 ms / 150 ms
Scheme selection

SS:1	T1	BO1
S1	x ¹	CBFP
S2 - S5		x
Master Trip (BI2)	x	

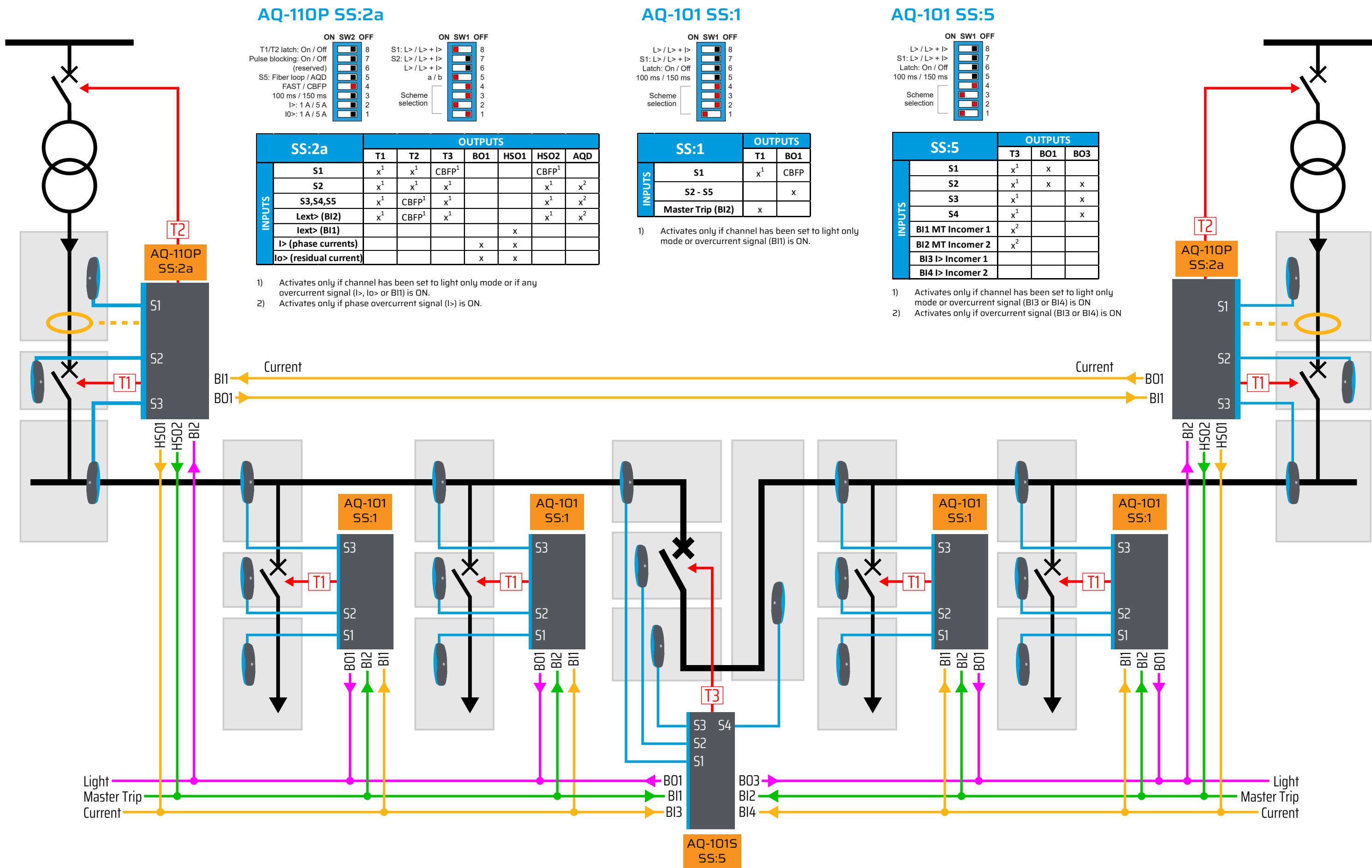
- 1) Activates only if channel has been set to light only mode or overcurrent signal (BI1) is ON.

AQ-101 SS:5

ON SW1 OFF
L> / L> + I>
S1: L> / L> + I>
Latch: On / Off
100 ms / 150 ms
Scheme selection

SS:5	T3	BO1	BO3
S1	x ¹	x	
S2	x ¹	x	x
S3	x ¹		
S4	x ¹		x
BI1 MT Incomer 1	x ²		
BI2 MT Incomer 2	x ²		
BI3 I> Incomer 1			
BI4 I> Incomer 2			

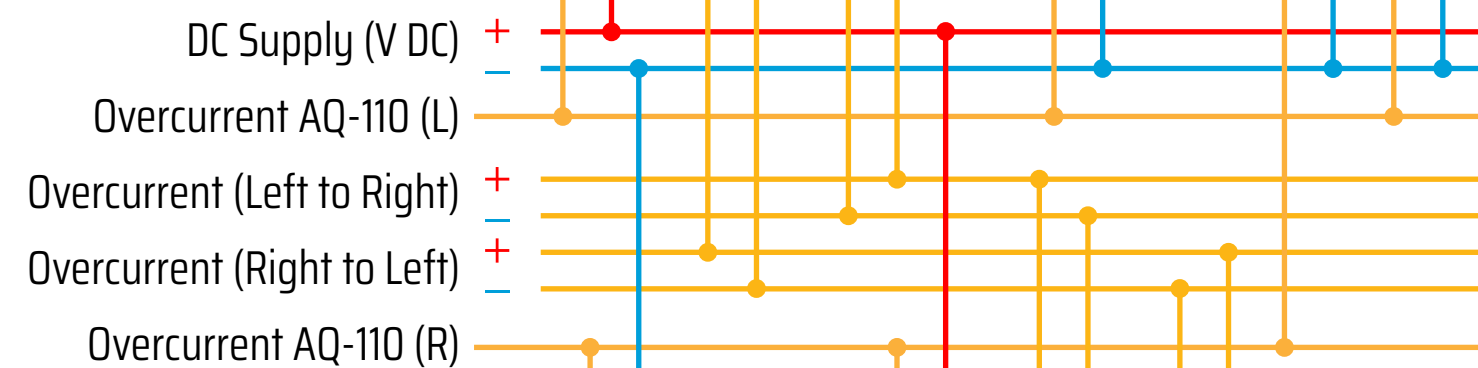
- 1) Activates only if channel has been set to light only mode or overcurrent signal (BI3 or BI4) is ON
- 2) Activates only if overcurrent signal (BI3 or BI4) is ON



AQ-110P SS:2a

AQ-101 SS:1 AQ-101S SS:5

Left side



Right side

AQ-110P SS:2a

AQ-101 SS:1

AQ-101S SS:5

Left side



Right side

AQ-101 SS:1

AQ-110P SS:2a